EMELIAS SAGA

		SPOS SUNISMIS	
USN			15CS72
		Seventh Semester B.E. Degree Examination, June/July 202	3
Advanced Computer Architecture			
Tin	ne: í	Max. M	larks: 80
Note: Answer any FIVE full questions, choosing ONE full question from each module.			
		Module-1	
1	a.	Explain elements of modern computers.	(04 Marks)
	b.	Explain how performance factors are influenced by the system attributes. What	
	c.	attributes mention in detail. Explain SIMD machine model with neat sketch.	(07 Marks) (05 Marks)
	С.	Explain Shvib machine insert with near sketch.	(05 Marks)
		OR	
2	a.	Explain static interconnection networks. What are system interconnection networks	
	b.	Explain efficiency utilization and quality for evaluating parallel computer models	(08 Marks) . (08 Marks)
		Module-2	
3	a.	Explain architectural distinctions between CISC and RISC processors with neat si	ketch. (04 Marks)
	b.	Explain the Sun Microsystem SPARC architecture with neat sketch. Explain in	
		scalar processors.	(05 Marks)
	c.	What are superscalar processors? Explain in brief the power architecture of the IE	
		superscalar processors with neat sketch.	(07 Marks)
		OR	
4	a.	Explain the properties of memory technology Inclusion Coherence and Locality.	(08 Marks)
	b.	Explain TLB, Paging and Segmentation in virtual memory management.	(08 Marks)
		Module-3	
5	a.	Explain Arbitration Transaction and Interrupt of the Bus system.	(07Marks)
	b.	Explain shared memory organization and memory interleaving in multiprocessor	models.
		Write a note on: (i) Sequential (ii) Weak consistency memory models.	(04 Marks) (05 Marks)
	c.,	write a note on . (1) sequential (11) weak consistency memory models.	(US Marks)
		OR	
6	a.	Explain speedup, efficiency and throughput of a linear pipeline processors.	(08 Marks)
	b.	Explain pipeline unit for fixed-point multiplication of 8-bit integers with neat ske	tch. (08 Marks)
			(00 Marks)
		Module-4	
7	a.	Explain (i) Omega (ii) Butterfly multistage networks with examples.	(08 Marks)
	b.	What are multicast routing algorithm explain in multiprocessors?	(08 Marks)
OR			
8	a.	Explain multi-vector multiprocessing architectural design concepts.	(08 Marks)
	b.	Explain principles of multithreading architecture in multiprocessor systems.	(08 Marks)

Module-5

Explain Parallel Programming models.

(04 Marks)

Explain optimizing compilers for parallelism.

(05 Marks)

What are the constructs of parallel programming languages? Explain.

(07 Marks)

Explain software tool types for parallel programming environments, with example. (08 Marks) 10

What are the principles of synchronization mechanism in parallel programming and explain (08 Marks) multiprocessor execution models.